

Advertisement No. LPU/DRD/50/1/5/3

ADVERTISEMENT FOR STUDENT INTERNSHIP UNDER SSR-SERB

Applications are invited for one post of <u>Student Internship</u> in the following project.

Title of the Project	Investigation of electrical noise for different geometries of Tunnel FETs devices for IoT applications
Funding Agency & Scheme	SSR-SERB (DST)
Principal Investigator	Dr. Sweta Chander
Department	Department of Innovation, School of Electronics and Electrical Engineering
Duration of the Project	2 Months (8 weeks)
Job Title	Student Internship
Emoluments	5000/- per month
	The intern's progress will be assessed each month by appropriate committee following DST-SERB project guidelines. If the progress is not satisfactory the internship is liable to beterminated.
Eligibility	The students in the final year of their PG or Final/Pre-Final year in UG are encouraged apply. Branches: B.Tech. (ECE/EEE/EE/CSE/IT) / M.Sc./M.Sc. (Integrated) (Electronics) M.Tech./ME (VLSI/ECE)
Desirable Quality	Candidate should have • Good in basic electronics • Good in mathematics • Good communication and writing skills
Selection Criteria	Students will be selected based on their academic profile and research proposal. Selected candidates will be intimated by email only regarding their selection.
Other information	Please fill out the following survey form latest by 28th August 2022: <u>https://forms.gle/2uNqR3XVc95GnyLC7</u> Incomplete applications/ applications received after the last date will not be entertained

Note:

- 1) The interview date will be communicated to the shortlisted candidate through E-mail.
- 2) No TA/ DA will be given for attending the interview.
- 3) The candidates are supposed to join immediately if selected.